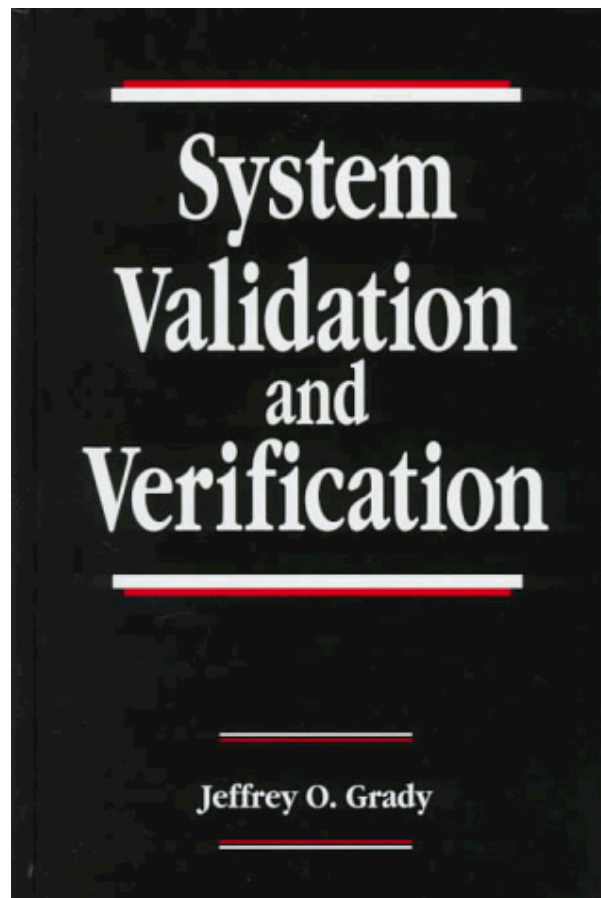


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Historically, the terms validation and verification have been very loosely defined in the system engineering world, with predictable confusion. Few hardware or software testing texts even touch upon validation and verification, despite the fact that, properly employed, these test tools offer system and test engineers powerful techniques for identifying and solving problems early in the design process. Together, validation and verification encompass testing, analysis, demonstration, and examination methods used to determine whether a proposed design will satisfy system requirements.

System Validation and Verification clear definitions of the terms and detailed information on using these fundamental tools for problem solving. It smoothes the transition between requirements and design by providing methods for evaluating the ability of a given approach to satisfy demanding technical requirements. With this book, system and test engineers and project managers gain confidence in their designs and lessen the likelihood of serious problems cropping up late in the program. In addition to explanations of the theories behind the concepts, the book includes practical methods for each step of the process, examples from the author's considerable experience, and illustrations and tables to support the ideas.

Although not primarily a textbook, System Validation and Verification is based in part on validation and verification courses taught by the author and is an excellent supplemental reference for engineering students. In addition to its usefulness to system engineers, the book will be valuable to a wider audience including manufacturing, design, software, and risk management project engineers - anyone involved in large systems design projects.

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Poorly written with good content hidden

By Robert

This is a book that covers very important topics in systems engineering. As such I would have thought an editor would be used when writing it. Instead it is a technical book that reads like it is written by a person

who has never written before. The whole book seems to be more like a first pass at a book rather than a finished product. I had to get this book for a class at school, but if I had gotten a chance to look at it more closely I doubt I would have even bought it for the class.

2 of 2 people found the following review helpful.

geared to US government standards

By W Boudville

Grady explains what it means to validate and verify a complex engineering system. While much of it can be used by project and system engineers anywhere in the world, be aware that some sections are specific to the United States. For example, there is a recap on the US military standards and the US Department of Defense formats for verification and documentation. Hence, you will see frequent references to specific MIL-STD documents.

Unsurprisingly, several of the examples are drawn from the US aerospace industry.

The book is not a light read. Reflecting the complexity of most realistic projects. There are many flowcharts, that greatly help in understanding the logic flow. The system verification is contained in several chapters dealing with system testing. There is an intricate methodology to try to test as many facets of the system as possible.

1 of 1 people found the following review helpful.

Great Content, Poor Production

By Science Reader

The content material is very well organized and cogently written. The main issue is the poor print job. The book was obviously printed "just-in-time" for the purchase. The graphics are poorly reproduced, the tables are printed badly (blurred, poor contrast, undersized font size). There was no quality assurance done to ensure the text was legible. Considering the high cost of this book, CRC Press should have provided a better quality edition. I am disappointed and will not buy CRC books in the future.

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